



# High-Speed CMOS 128Kx8 SRAM

QS812880  
ADVANCE  
INFORMATION

## FEATURES/BENEFITS

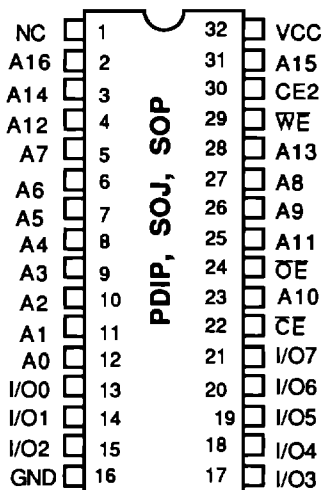
- Equal access and cycle times
- 20ns/25ns/30 ns Commercial
- 25ns/30 ns Military
- Common I/O with output enable
- Military product compliant to MIL-STD-883
- High performance QCMOS™ technology
- Dual chip select
- 6-Transistor cell for high reliability
- TTL compatible I/O
- High performance QCMOS™ technology
- Available in 400-mil 32-pin PDIP, 32-pin SOP, & 32-pin SOJ with JEDEC standard pinout

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## DESCRIPTION

The QS812880 is a high-speed 1024K SRAM organized as 128K words of 8 bits. It has two chip select control lines, and common I/O with output enable. It is manufactured in a high-performance CMOS process, and it based on a 6-transistor cell design for high reliability of data retention. The high-speed access times of the QS81280 make it useful in cache data RAM, cache tag RAMs, high-speed scratchpad memories, look-up tables, pipelined DSP and bit-slice systems. Low operating power and excellent latch-up and ESD protection are provided.

## PIN CONFIGURATIONS



ALL PINS TOP VIEW